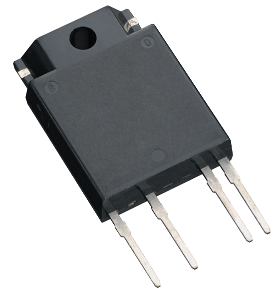
S102S02 Series S202S02 Series

I_T(rms)≤8A, Zero Cross type SIP 4pin Triac output SSR

*Non-zero cross type is also available. (**S102S01 Series/ S202S01 Series**)



■ Description

S102S02 Series and **S202S02 Series** Solid State Relays (SSR) are an integration of an infrared emitting diode (IRED), a Phototriac Detector and a main output Triac. These devices are ideally suited for controlling high voltage AC loads with solid state reliability while providing 4.0kV isolation (V_{iso}(rms)) from input to output.

■ Features

1. Output current, I_T(rms)≤8.0A
2. Zero crossing functionary (V_{OX} : MAX. 35V)
3. 4 pin SIP package
4. High repetitive peak off-state voltage
   (V_{DRM} : 600V, **S202S02 Series**)
   (V_{DRM} : 400V, **S102S02 Series**)
5. High isolation voltage between input and output (V_{iso}(rms) : 4.0kV)
6. Lead-free terminal components are also available (see Model Line-up section in this datasheet)
7. Screw hole for heat sink

■ Agency approvals/Compliance

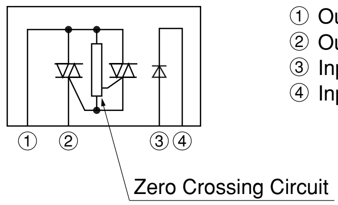
1. Recognized by UL508, file No. E94758 (as models No. **S102S02/S202S02**)
2. Approved by CSA 22.2 No.14, file No. LR63705 (as models No. **S102S02/S202S02**)
3. Package resin : UL flammability grade (94V-0)

■ Applications

1. Isolated interface between high voltage AC devices and lower voltage DC control circuitry.
2. Switching motors, fans, heaters, solenoids, and valves.
3. Power control in applications such as lighting and temperature control equipment.

Sheet No.:D4-A02401EN
Date Apr. 28. 2004

■ Internal Connection Diagram

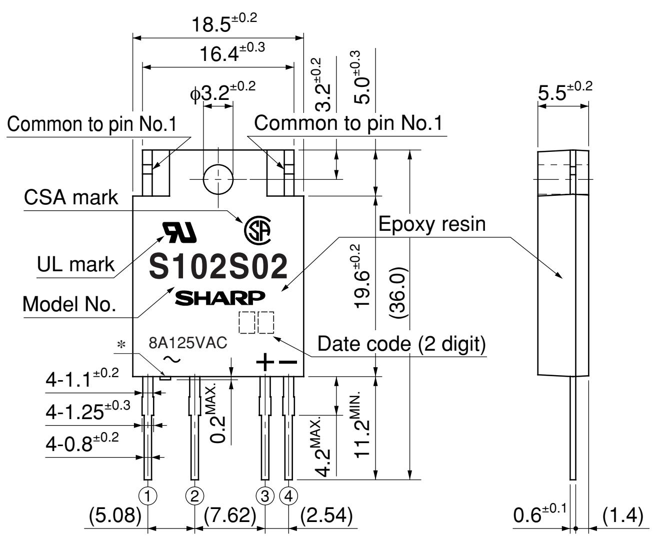


① Output (Triac T2)
② Output (Triac T1)
③ Input (+)
④ Input (–)

■ Outline Dimensions

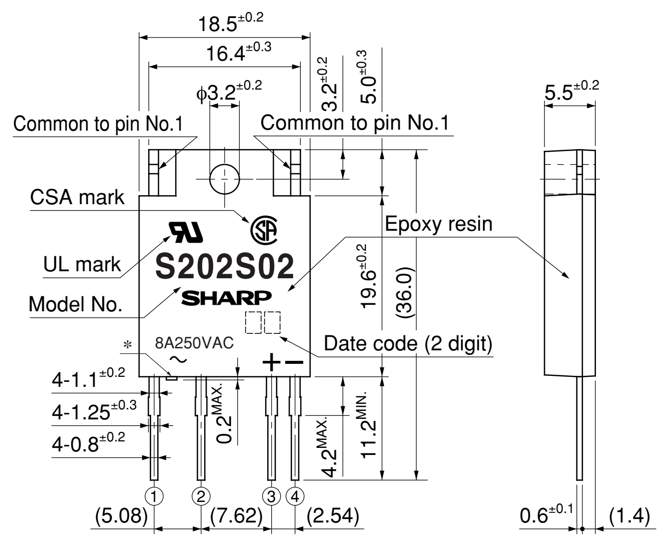
(Unit : mm)

S102S02



Product mass : approx. 6.3g

S202S02



Product mass : approx. 6.3g

* : Do not allow external connection.
( ) : Typical dimensions

Date code (2 digit)

1st digit				2nd digit	
Year of production				Month of production	
A.D.	Mark	A.D	Mark	Month	Mark
1990	A	2002	P	January	1
1991	B	2003	R	February	2
1992	C	2004	S	March	3
1993	D	2005	T	April	4
1994	E	2006	U	May	5
1995	F	2007	V	June	6
1996	H	2008	W	July	7
1997	J	2009	X	August	8
1998	K	2010	A	September	9
1999	L	2011	B	October	O
2000	M	2012	C	November	N
2001	N	⋮	⋮	December	D

repeats in a 20 year cycle

Country of origin

Japan

Rank mark

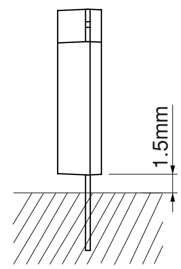
There is no rank mark indicator and currently there are no rank offered for this device.

■ Absolute Maximum Ratings

(T_a=25˚C)

Parameter			Symbol	Rating	Unit
Input	Forward current		I_F	50 *3	mA
	Reverse voltage		V_R	6	V
Output	RMS ON-state current		I_T(rms)	8 *3	A
	Peak one cycle surge current		I_{surge}	80 *4	A
	Repetitive peak OFF-state voltage	S102S02	V_{DRM}	400	V
		S202S02		600	
	Non-Repetitive peak OFF-state voltage	S102S02	V_{DSM}	400	V
		S202S02		600	
	Critical rate of rise of ON-state current		dI_T/dt	50	A/μs
	Operating frequency		f	45 to 65	Hz
*1Isolation voltage			V_{iso}(rms)	4.0	kV
Operating temperature			T_{opr}	−25 to +100	˚C
Storage temperature			T_{stg}	−30 to +125	˚C
*2Soldering temperature			T_{sol}	260	˚C

*1 40 to 60%RH, AC for 1minute, f=60Hz
*2 For 10s
*3 Refer to Fig.1, Fig.2
*4 f=50Hz sine wave, T_j=25˚C start



Soldering area

■ Electro-optical Characteristics

(T_a=25˚C)

Parameter			Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Input	Forward voltage		V_F	I_F=20mA	−	1.2	1.4	V
	Reverse current		I_R	V_R=3V	−	−	100	μA
Output	Repetitive peak OFF-state current		I_{DRM}	$V_D=V_{DRM}$	−	−	100	μA
	ON-state voltage		V_T(rms)	I_T(rms)=2A, Resistance load, I_F=20mA	−	−	1.5	V
	Holding current		I_H	−	−	−	50	mA
	Critical rate of rise of OFF-state voltage		dV/dt	V_D=2/3•V_{DRM}	30	−	−	V/μs
	Critical rate of rise of OFF-state voltage at commutaion		(dV/dt)c	T_j=125˚C, V_D=2/3•V_{DRM}, dI_T/dt=−4.0A/ms	5	−	−	V/μs
Transfer charac-teristics	Minimum trigger current		I_{FT}	V_D=6V, R_L=30Ω	−	−	8	mA
	Isolation resistance		R_{ISO}	DC500V, 40 to 60%RH	10^{10}	−	−	Ω
	Zero cross voltage		V_{OX}	I_F=8mA	−	−	35	V
	Turn-on time	S102S02	t_{on}	V_D(rms)=100V, AC50Hz I_T(rms)=2A, Resistance load, I_F=20mA	−	−	10	ms
		S202S02		V_D(rms)=200V, AC50Hz I_T(rms)=2A, Resistance load, I_F=20mA	−	−	10	
	Turn-off time	S102S02	t_{off}	V_D(rms)=100V, AC50Hz I_T(rms)=2A, Resistance load, I_F=20mA	−	−	10	ms
		S202S02		V_D(rms)=200V, AC50Hz I_T(rms)=2A, Resistance load, I_F=20mA	−	−	10	
Thermal resistance			R_{th}(j-c)	Between junction and case	−	4.5	−	˚C/W
			R_{th}(j-a)	Between junction and ambient	−	40	−	

■ Model Line-up (1) (Lead-free terminal components)

		V_{DRM} [V]	I_{FT}[mA] (V_D=6V, R_L=30Ω)
Shipping Package	Case		
	200pcs/case		
Model No.	**S102S02F**	400	MAX.8
	S202S02F	600	MAX.8

■ Model Line-up (2) (Lead solder plating components)

		V_{DRM} [V]	I_{FT}[mA] (V_D=6V, R_L=30Ω)
Shipping Package	Case		
	200pcs/case		
Model No.	**S102S02**	400	MAX.8
	S202S02	600	MAX.8

Please contact a local SHARP sales representative to see the actual status of the production.

Fig.1 Forward Current vs. Ambient Temperature

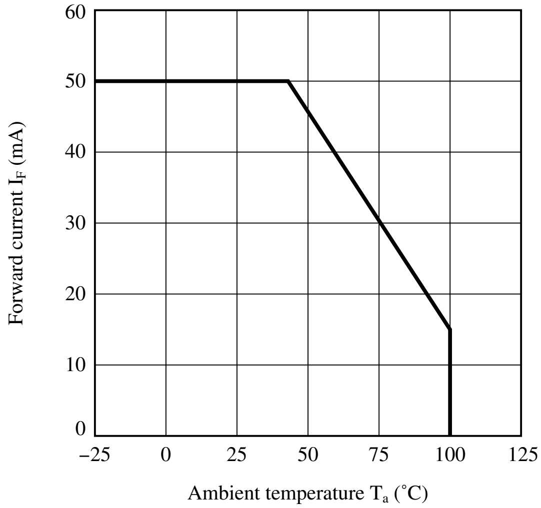
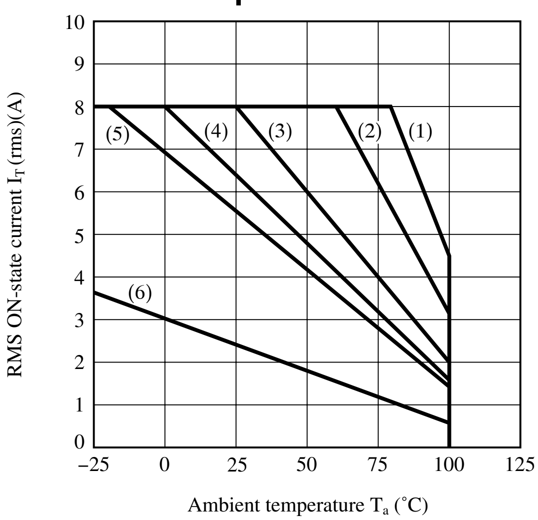


Fig.2 RMS ON-state Current vs. Ambient Temperature



(1) With infinite heat sink
(2) With heat sink (200×200×2mm Al plate)
(3) With heat sink (100×100×2mm Al plate)
(4) With heat sink (75×75×2mm Al plate)
(5) With heat sink (50×50×2mm Al plate)
(6) Without heat sink
(Note) In natural cooling condition, please locate Al plate vertically, spread the thermal conductive silicone grease on the touch surface of the device and tighten up the device in the center of Al plate at the torque of 0.4N•m.

Fig.3 RMS ON-state Current vs. Case Temperature

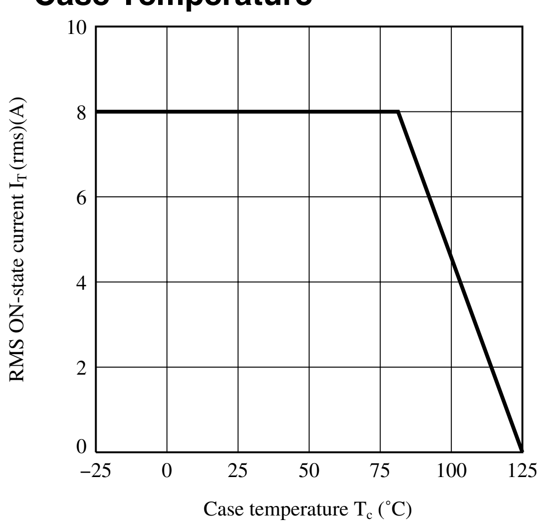


Fig.4 Forward Current vs. Forward Voltage

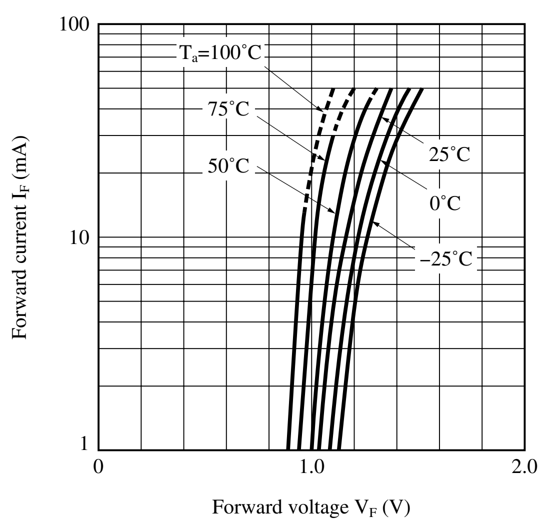


Fig.5 Surge Current vs. Power-on Cycle

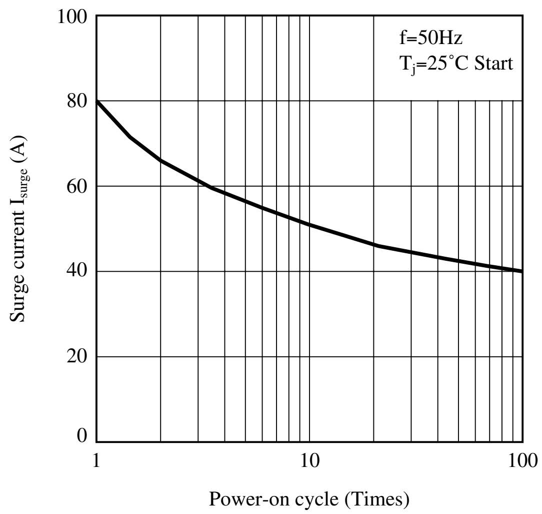


Fig.6 Maximum ON-state Power Dissipation vs. RMS ON-state Current

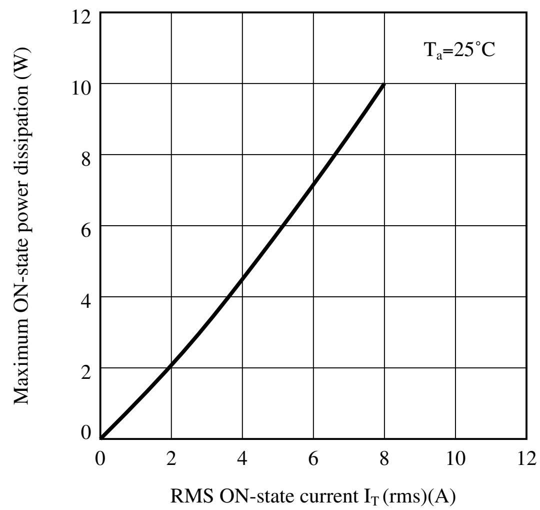


Fig.7 Minimum Trigger Current vs. Ambient Temperature

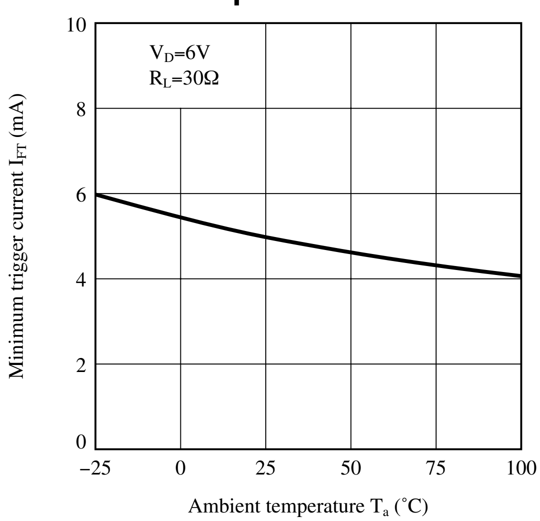
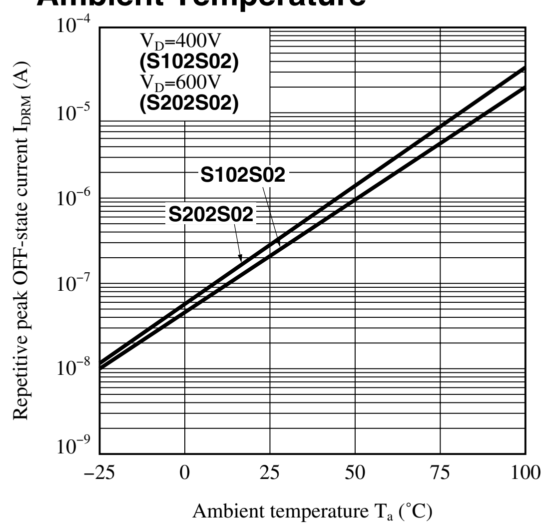


Fig.8 Repetitive Peak OFF-state Current vs. Ambient Temperature



Remarks : Please be aware that all data in the graph are just for reference.

■ Design Considerations

● Recommended Operating Conditions

Parameter			Symbol	Conditions	MIN.	MAX.	Unit
Input	Input signal current at ON state		I_F(ON)	–	16	24	mA
	Input signal current at OFF state		I_F(OFF)	–	0	0.1	mA
Output	Load supply voltage	**S102S02**	V_{OUT}(rms)	–	80	120	V
		S202S02			80	240	
	Load supply current		I_{OUT}(rms)	Locate snubber circuit between output terminals (Cs=0.022μF, Rs=47Ω)	0.1	I_T(rms) ×80%(*)	mA
	Frequency		f	–	47	63	Hz
Operating temperature			T_{opr}	–	–20	80	˚C

(*) See Fig.2 about derating curve (I_T(rms) vs. ambient temperature).

● Design guide

In order for the SSR to turn off, the triggering current (I_F) must be 0.1mA or less.

When the input current (I_F) is below 0.1mA, the output Triac will be in the open circuit mode. However, if the voltage across the Triac, V_D, increases faster than rated dV/dt, the Triac may turn on. To avoid this situation, please incorporate a snubber circuit. Due to the many different types of load that can be driven, we can merely recommend some circuit vales to start with : Cs=0.022μF and Rs=47Ω. The operation of the SSR and snubber circuit should be tested and if unintentional switching occurs, please adjust the snubber circuit component values accordingly.

When making the transition from On to Off state, a snubber circuit should be used ensure that sudden drops in current are not accompanied by large instantaneous changes in voltage across the Triac.
This fast change in voltage is brought about by the phase difference between current and voltage.
Primarily, this is experienced in driving loads which are inductive such as motors and solenoids.
Following the procedure outlined above should provide sufficient results.

For over voltage protection, a Varistor may be used.

Any snubber or Varistor used for the above mentioned scenarios should be located as close to the main output triac as possible.

Particular attention needs to be paid when utilizing SSRs that incorporate zero crossing circuitry.
If the phase difference between the voltage and the current at the output pins is large enough, zero crossing type SSRs cannot be used. The result, if zero crossing SSRs are used under this condition, is that the SSR may not turn on and off irregardless of the input current. In this case, only a non zero cross type SSR should be used in combination with the above mentioned snubber circuit selection process.

The load current should be within the bounds of derating curve. (Refer to Fig.2)
Also, please use the optional heat sink when necessary.

In case the optional heat sink is used and the isolation voltage between the device and the optional heat sink is needed, please locate the insulation sheet between the device and the heat sink.

When the optional heat sink is equipped, please set up the M3 screw-fastening torque at 0.3 to 0.5N•m.
In order to dissipate the heat generated from the inside of device effectively, please follow the below suggestions.

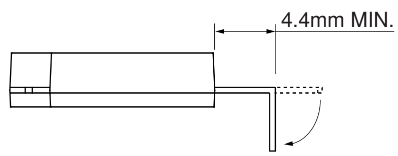
(a) Make sure there are no warps or bumps on the heat sink, insulation sheet and device surface.
(b) Make sure there are no metal dusts or burrs attached onto the heat sink, insulation sheet and device surface.
(c) Make sure silicone grease is evenly spread out on the heat sink, insulation sheet and device surface.

Silicone grease to be used is as follows;
1) There is no aged deterioration within the operating temperature ranges.
2) Base oil of grease is hardly separated and is hardly permeated in the device.
3) Even if base oil is separated and permeated in the device, it should not degrade the function of a device.

Recommended grease : G-746 (Shin-Etsu Chemical Co., Ltd.)
: G-747 (Shin-Etsu Chemical Co., Ltd.)
: SC102 (Dow Corning Toray Silicone Co., Ltd.)

In case the optional heat sink is screwed up, please solder after screwed.

In case of the lead frame bending, please keep the following minimum distance and avoid any mechanical stress between the base of terminals and the molding resin.

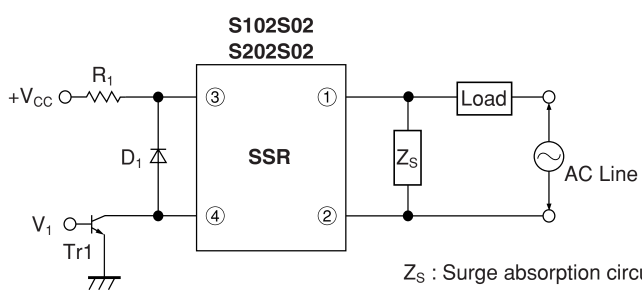


Some of AC electromagnetic counters or solenoids have built-in rectifier such as the diode.
In this case, please use the device carefully since the load current waveform becomes similar with rectangular waveform and this results may not make a device turn off.

● Degradation

In general, the emission of the IRED used in SSR will degrade over time.
In the case where long term operation and / or constant extreme temperature fluctuations will be applied to the devices, please allow for a worst case scenario of 50% degradation over 5years.
Therefore in order to maintain proper operation, a design implementing these SSRs should provide at least twice the minimum required triggering current from initial operation.

● Standard Circuit



Z_S : Surge absorption circuit (Snubber circuit)

☆ For additional design assistance, please review our corresponding Optoelectronic Application Notes.

■ Manufacturing Guidelines

● Soldering Method

Flow Soldering (No solder bathing)

Flow soldering should be completed below 260˚C and within 10s.
Preheating is within the bounds of 100 to 150˚C and 30 to 80s.
Please solder within one time.

Other notices

Please test the soldering method in actual condition and make sure the soldering works fine, since the impact on the junction between the device and PCB varies depending on the tooling and soldering conditions.

● Cleaning instructions

Solvent cleaning :

Solvent temperature should be 45˚C or below. Immersion time should be 3minutes or less.

Ultrasonic cleaning :

The impact on the device varies depending on the size of the cleaning bath, ultrasonic output, cleaning time, size of PCB and mounting method of the device.
Therefore, please make sure the device withstands the ultrasonic cleaning in actual conditions in advance of mass production.

Recommended solvent materials :

Ethyl alcohol, Methyl alcohol and Isopropyl alcohol.
In case the other type of solvent materials are intended to be used, please make sure they work fine in actual using conditions since some materials may erode the packaging resin.

● Presence of ODC

This product shall not contain the following materials.
And they are not used in the production process for this device.
Regulation substances : CFCs, Halon, Carbon tetrachloride, 1.1.1-Trichloroethane (Methylchloroform)
Specific brominated flame retardants such as the PBBOs and PBBs are not used in this product at all.

■ Package specification

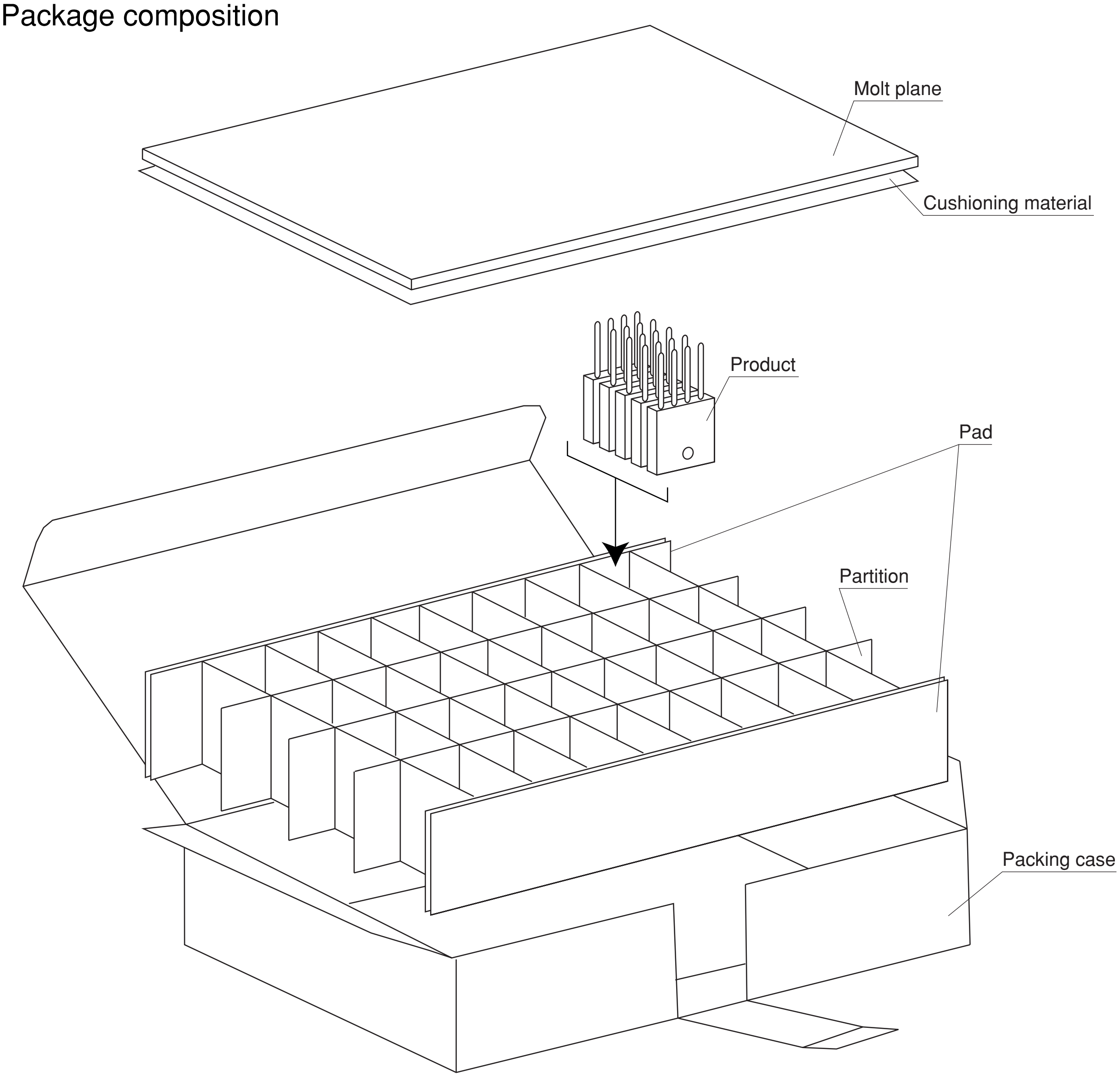
Package materials

Packing case : Corrugated cardboard
Partition : Corrugated cardboard
Pad : Corrugated cardboard
Cushioning material : Polyethylene
Molt plane : Urethane

Package method

The product should be located after the packing case is partitioned and protected inside by 4 pads.
Each partition should have 5 products with the lead upward.
Cushioning material and molt plane should be located after all products are settled (1 packing contains 200 pcs).

Package composition



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(i) The devices in this publication are designed for use in general electronic equipment designs such as:

--- Personal computers
--- Office automation equipment
--- Telecommunication equipment [terminal]
--- Test and measurement equipment
--- Industrial control
--- Audio visual equipment
--- Consumer electronics

(ii) Measures such as fail-safe function and redundant design should be taken to ensure reliability and safety when SHARP devices are used for or in connection with equipment that requires higher reliability such as:

--- Transportation control and safety equipment (i.e., aircraft, trains, automobiles, etc.)
--- Traffic signals
--- Gas leakage sensor breakers
--- Alarm equipment
--- Various safety devices, etc.

(iii) SHARP devices shall not be used for or in connection with equipment that requires an extremely high level of reliability and safety such as:

--- Space applications
--- Telecommunication equipment [trunk lines]
--- Nuclear power control equipment
--- Medical and other life support equipment (e.g., scuba).

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